



Test Report : SMU01N-15

1W DC-DC Unregulated Single Output Converter

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

■ SAFETY TEST

Safety Test

DESIGN VERIFY TEST
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	VOLTAGE ACCURACY	-2%~+2% (Max)	I/P:24VDC O/P:100% LOAD Ta:25°C	+0.22%	P
2	RIPPLE & NOISE	100mVp-p (Max)	I/P:24VDC O/P:FULL LOAD Ta:25°C	18.8mV	P
3	LINE REGULATION	-12%~+12% (Max)	I/P:21.6VDC~26.4VDC O/P:FULL LOAD Ta:25°C	-10.78% ~ +9.66%	P
4	LOAD REGULATION	-8.0%~+8.0% (Max)	I/P:24VDC O/P:MIN-FULL LOAD Ta:25°C	-0.84% ~ +1.04%	P

INPUT FUNCTION TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	EFFICIENCY	77% (Typ)	I/P:24VDC O/P:FULL LOAD Ta:25°C	77.72%	P
2	DC CURRENT	57 mA / FULL LOAD (Max) 11 mA / NO LOAD (Max)	I/P:24VDC O/P:NO / FULL LOAD Ta:25°C	54 mA / FULL LOAD 10 mA / NO LOAD	P

SAFETY TEST
SAFETY TEST

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	WITHSTAND VOLTAGE	I/P-O/P:1500 VDC/min	I/P-O/P:1500 VDC/min Ta:25°C	I/P-O/P: 0.02mA NO DAMAGE	P
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>100MΩ	I/P-O/P:500 VDC Ta:25°C	I/P-O/P>100MΩ NO DAMAGE	P

DATE	SAMPLE	TEST RESULT	TESTER	APPROVAL
2009.9.5	PRODUCT SAMPLE	PASS	PETER	VINCENT ZENG